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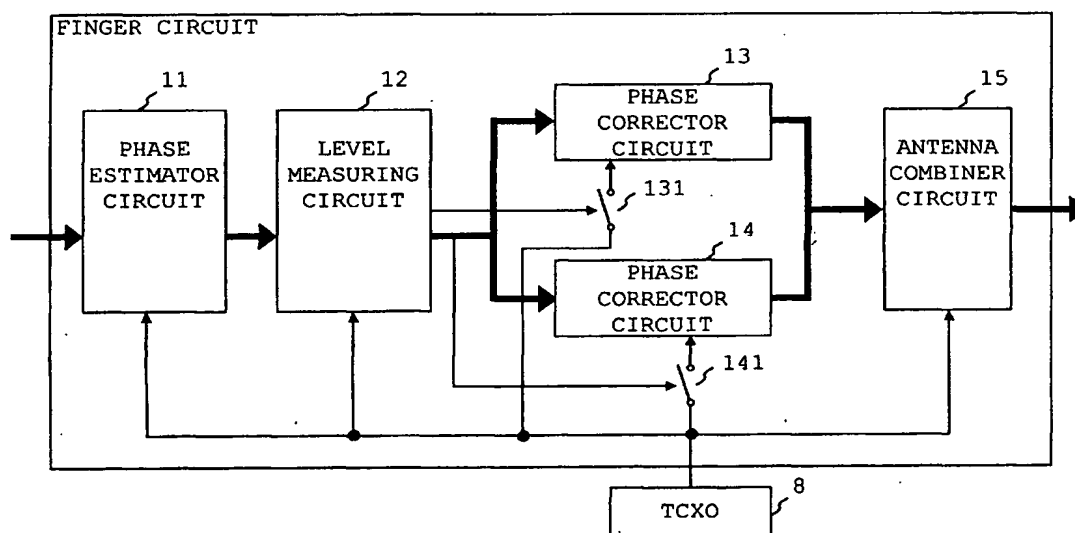
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(54) CDMA receiver terminal with reduced power consumption in diversity mode

(57) A CDMA receiver terminal comprises a finger circuit for correcting the phase of inverse spread data for signals from two antennas of a base station, and the finger circuit in turn has a level measuring circuit (12) for determining the validity of the two signals. The level

measuring circuit, when determining that any of the received signals from the two antennas of the base station is invalid, stops supplying an operating clock from a clock supply circuit (8) to a phase corrector circuit (13,14) which corrects the phase of the signal determined as invalid.

Fig. 5



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EUROPEAN SEARCH REPORT

Application Number
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Place of search MUNICH		Date of completion of the search 20 January 2003	Examiner Galli, P
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The members are as contained in the European Patent Office EDP file on
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